

PSM712

Transient Voltage Suppressor

SOT23

Features

- 400 watts peak pulse power (t_p = 8/20µs)
- Protects two -7V to 12V lines
- Low capacitance
- Low clamping voltage
- Solid-state silicon avalanche technology

IEC COMPATIBILITY (EN61000-4)

- IEC 61000-4-2 (ESD) ±15kV (air), ±8kV (contact)
- IEC 61000-4-4 (EFT) 40A (5/50ns)
- IEC 61000-4-5 (Lightning) 12A (8/20µs)

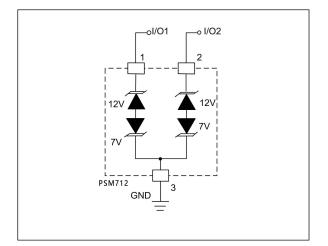
Mechanical Characteristics

- JEDEC SOT23 package
- Molding compound flammability rating: UL 94V-0
- Marking : Making Code
- Packaging : Tape and Reel per EIA 481

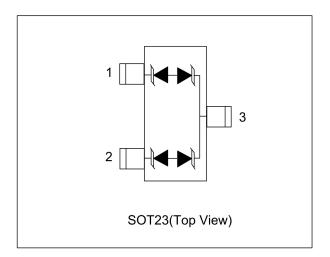
Applications

- Protection of RS-485 transceivers with extended common-mode range
- Security systems
- Automatic Teller Machines
- HFC systems
- Net works

Circuit Diagram



Schematic & PIN Configuration

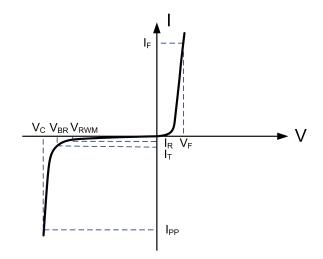


Absolute Maximum Rating

Rating	Symbol	Value	Units		
Peak Pulse Power (t _p =8/20µs)	P _{PP}	400	Watts		
Lead Soldering Temperature	ΤL	260(10 sec.)	°C		
Peak Pulse Current (t _p =8/20µs)	I _{pp}	12	А		
Operating Temperature	TJ	-55 to + 125	°C		
Storage Temperature	T _{STG}	-55 to +150	°C		

Electrical Parameters (T=25°C)

Symbol	Parameter	
PP	Maximum Reverse Peak Pulse Current	
Vc	Clamping Voltage @ IPP	
VRWM	Working Peak Reverse Voltage	
IR	Maximum Reverse Leakage Current @	
VBR	Breakdown Voltage @ I⊤	
Ιτ	Test Current	
lF	Forward Current	
VF	Forward Voltage @ I⊧	



Electrical Characteristics

PSM712									
	1	1	Pins 1 to 3 and 2 to 3 (12V TVS)		Pins 3 to 1 and 3 to 2(7V TVS)				
Parameter	Symbol	Conditions	MIN	ТҮР	MAX	MIN	ТҮР	MAX	Units
Reverse Stand-Off Voltage	V _{RWM}	Pin 3 to 1 or Pin 2 to 1			12			7	V
Reverse Breakdown Voltage	V_{BR}	I _{PT} = 1mA	13.3			7.5			V
Reverse Leakage Current	I _R	$V_{R} = V_{RWM}$			1			1	μA
Clamping Voltage	Vc	I _{PP} = 5A, tp = 8/20μs			20			10	V
Clamping Voltage	Vc	I _{PP} = 12A, tp = 8/20μs			26				V
lunction Consoltance	C	V _R = 0V, f = 1MHz			75			75	pF
Junction Capacitance	Cj	V _R = V _{RWM} , f = 1MHz		45			45		pF

PSM712

Typical Characteristics

Figure 1 Non-Repetitive Peak Pulse Power vs. Pulse Time

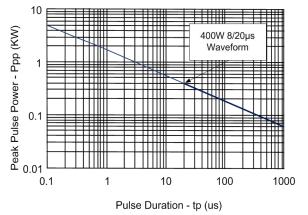


Figure 3 Pulse Waveform

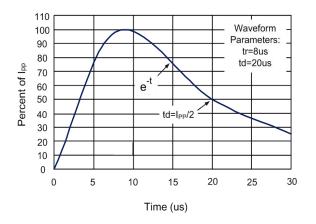


Figure 5 Capacitance vs. Reverse Voltage

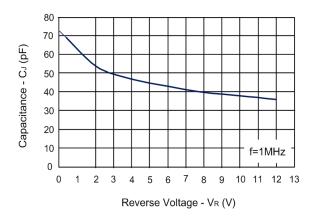


Figure 2 Power Derating curve

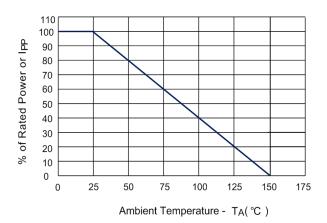
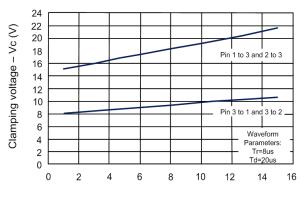


Figure 4 Clamping Voltage vs. Peak Pulse Current



Peak Pulse Current - Ipp (A)

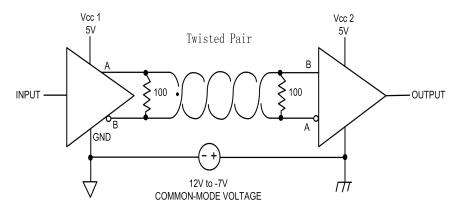
Application Information

Device Connection for Protection of Two RS-485 Data Lines

EIA RS-485 specifies a \pm 7V ground difference between devices on the bus. This permits the bus voltage to range from +12V (5V + 7V) to -7V (0 - 7V).

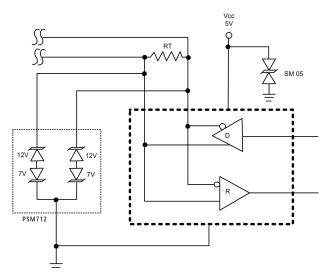
The PSM712 is designed to protect two RS-485 data lines in extended common mode applications. The PSM712 may be used to protect devices from transient voltages resulting from ESD, EFT, and lightning. The device is designed with asymmetrical operating voltages for optimum protection. The TVS diodes at pins 1 and 2 have a working voltage of 12 volts. These pins are connected to the differential data line pairs. The TVS diodes at pin 3 have a working voltage of 7 volts. Pin 3 is connected to ground. The internal TVS diodes of the PSM712 will protect the transceiver input from positive transient voltage spikes greater than 12V and Negative spikes greater than 7V.

A series current limiting resistor may be added in applications requiring enhanced surge immunity.



RS-485 Common Mode Voltages

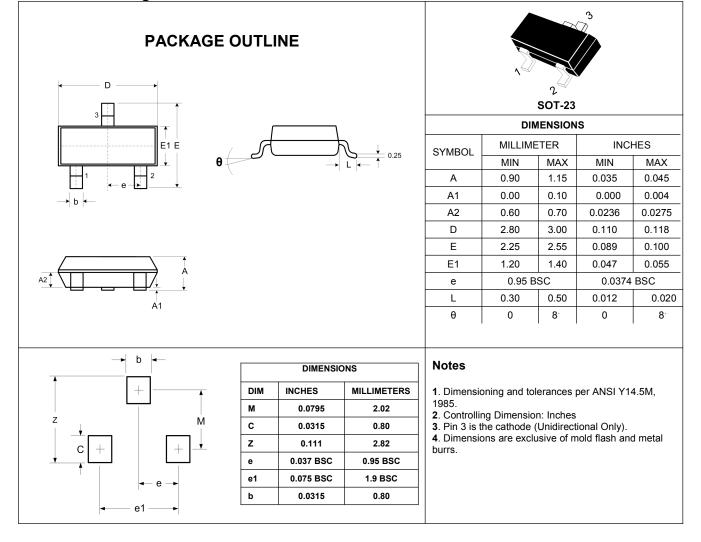
RS-485 Protection Circuit



Transient Voltage Suppressor



Outline Drawing – SOT23



Marking Codes

Part Number	PSM712
Marking Code	7AM

Package Information

Qty: 3k/Ree